

Appln No. 10/810,053

Amdt date March 16, 2005

Reply to Office action of November 30, 2004

**Amendments to the Specification:**

Page 15, after line 13, insert the following new paragraph:

To summarize the operation of the digital to analog converter described in connection with FIG. 9, a matrix of DAC cells are formed from different "anded" combinations of states of binary indications to be decoded. One such cell is shown in FIG. 9. Transistors 136 and 140 (together with complementary transistors 144 and 146) comprise one of the "anded" combinations. Transistors 138 and 139 (together with complementary transistors 148 and 149) comprise another of the "anded" combinations. The cells are sampled by clock actuated switches 134 and 142 and latched by transistors 110 and 116 and transistors 112 and 114. As represented in FIG. 9, the cells are directly connected to switches 134 and 142 without any intervening circuitry.